

Product Summary

V_{RRM} (V)	I_o (A)	V_F max (V)	I_R max (μ A)
100	6	0.74	0.1

Description

The SBR6100CTLQ is dual center tap rectifier in TO-252 (DPAK) package. Offering excellent high temperature stability and superior avalanche capability, this device is specifically intended for use in automotive applications.

Applications

- DC – DC Converters
- DC/AC Inverters
- AC/DC Power Supplies

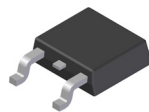
Features

- Low forward voltage drop (V_F); better efficiency and cooler operation.
- Reduced high temperature reverse leakage; Increased reliability against thermal runaway failure in high temperature operation
- Excellent High Temperature Stability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

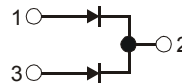
Mechanical Data

- Case: TO252 (DPAK)
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish - Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208 ③
- Polarity: See Below
- Weight: 0.33 grams (approximate)

TO252



Top View



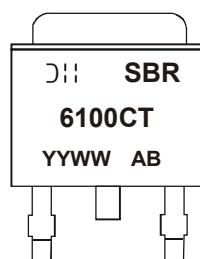
Package Pin-Out Configuration

Ordering Information (Note 4)

Part Number	Case	Packaging
SBR6100CTLQ-13	TO252	2500/Tape & Reel, 13-inch

- Notes:
1. EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant. All applicable RoHS exemptions applied.
 2. See <http://www.diodes.com> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information



6100CT = Product Type Marking Code
 AB = Foundry and Assembly Code
 YYWW = Date Code Marking
 YY = Last two digits of year (ex: 08 = 2008)
 WW = Week (01 - 53)

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{R(RM)}	100	V
Working Peak Reverse Voltage	V _{R(WM)}		
DC Blocking Voltage	V _{RM}		
RMS Reverse Voltage	V _{R(RMS)}	71	V
Average Rectified Output Current @ T _C = +115°C	I _O	6	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	78	A
Repetitive Peak Avalanche Power (1μs, +25°C)	P _{ARM}	7000	W
Non-Repetitive Avalanche Energy (T _J = +25°C, I _{AS} = 6A, L = 8.5mH)	E _{AS}	120	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance, Junction to Ambient (per leg) (Note 5)	R _{θJA}	49	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop (per leg)	V _F	-	0.68	0.74	V	I _F = 3A, T _J = +25°C
			0.56	0.62		I _F = 3A, T _J = +125°C
Leakage Current (Note 6) (per leg)	I _R	-	-	0.1 12	mA	V _R = 100V, T _J = +25°C V _R = 100V, T _J = +125°C

Notes: 5. Device mounted on Poly substrate PC board, 1oz copper, with minimum recommended pad layout.
6. Short duration pulse test used to minimize self-heating effect.

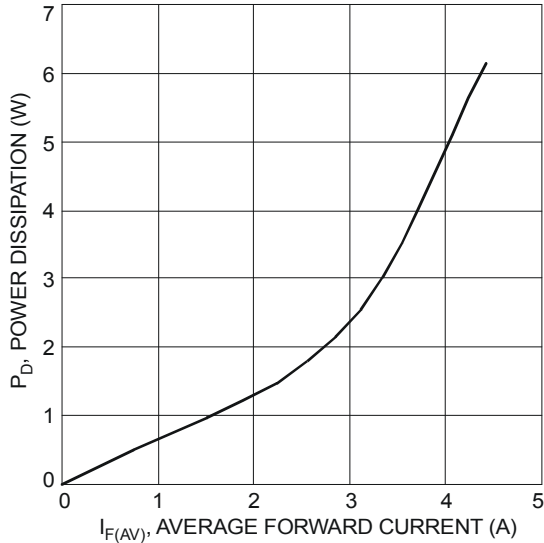


Figure 1 Forward Power Dissipation

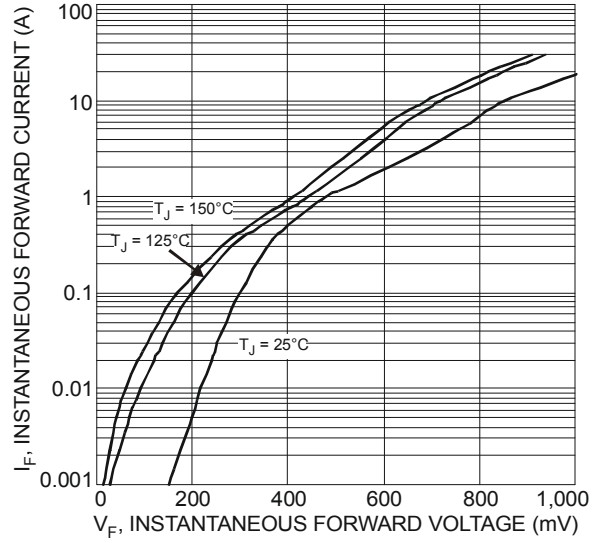


Figure 2 Typical Forward Characteristics

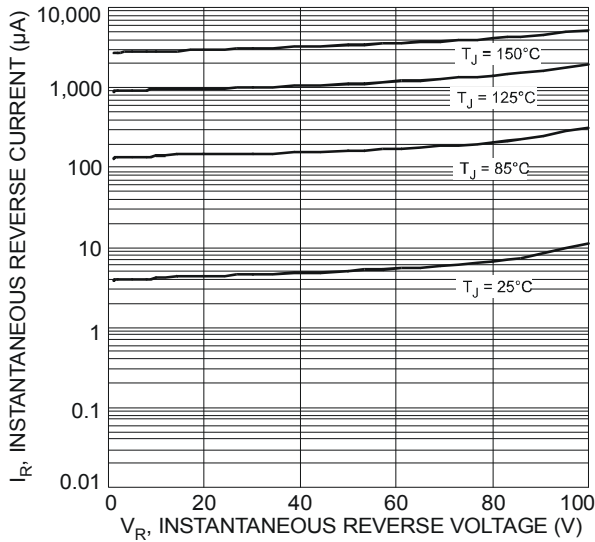


Figure 3 Typical Reverse Characteristics

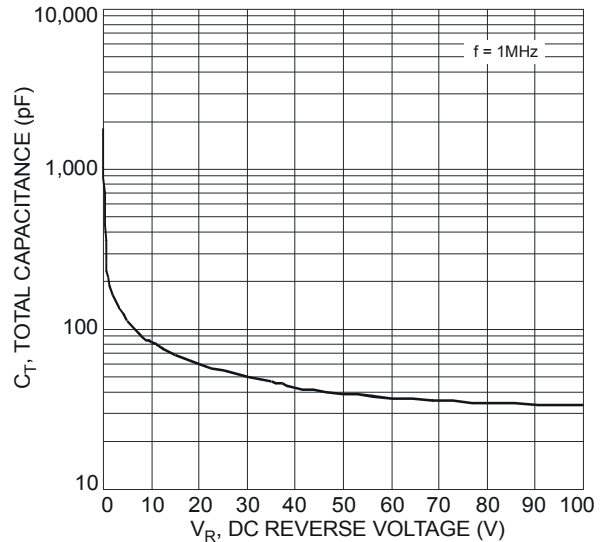


Figure 4 Total Capacitance vs. Reverse Voltage

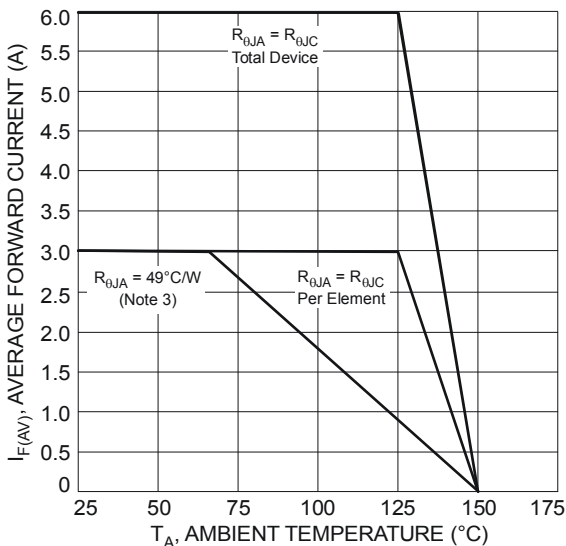


Figure 5 Forward Current Derating Curve

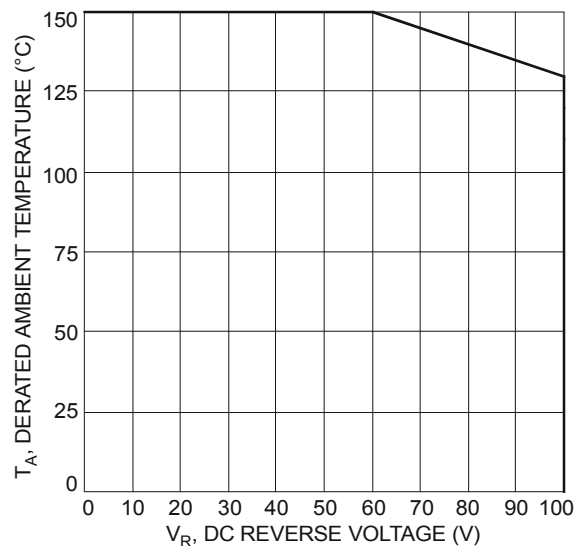


Figure 6 Operating Temperature Derating

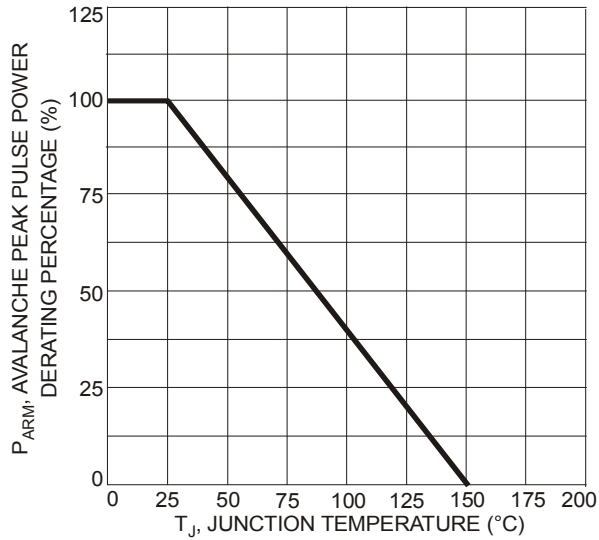


Figure 7 Pulse Derating Curve, Per Element

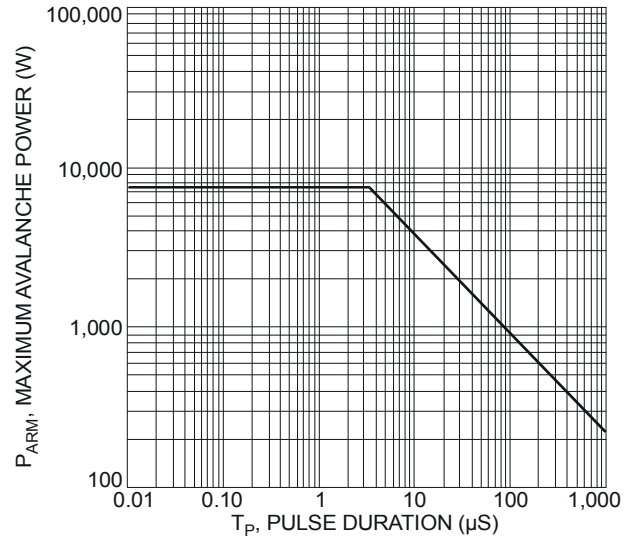


Figure 8 Maximum Avalanche Power Curve, Per Element

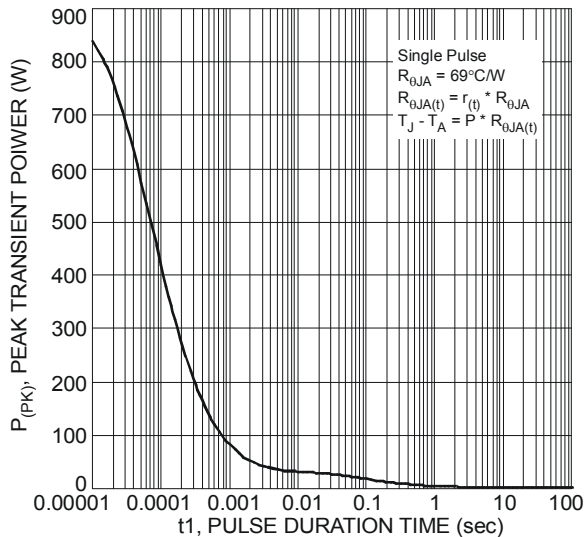


Figure 9 Single Pulse Maximum Power Dissipation

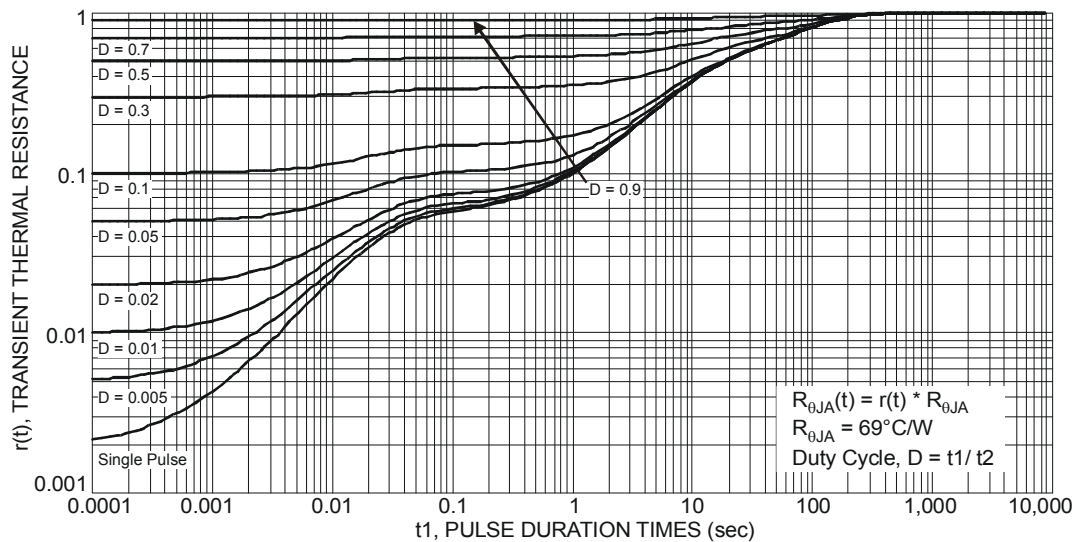
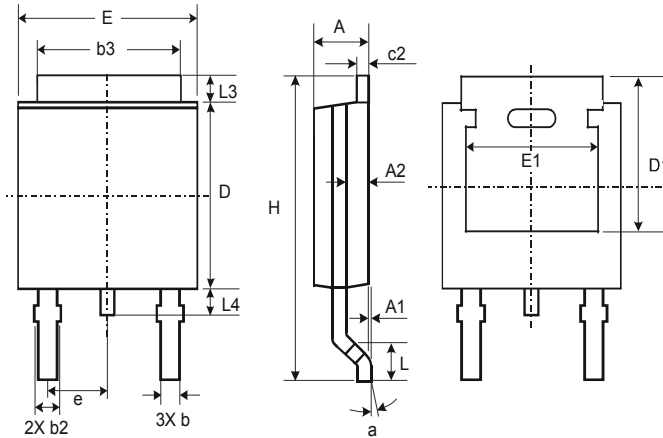


Figure 10 Transient Thermal Resistance

Package Outline Dimensions

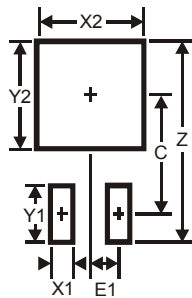
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for latest version.



TO252			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c2	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
Z	11.6
X1	1.5
X2	7.0
Y1	2.5
Y2	7.0
C	6.9
E1	2.3

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